	Roll No.	II - SinU	[Total No. of Pages : 2
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21	B.Tech. IV Semester (Main/Back) Examination 2012		
G	Electrical Engineering.		g.
4	4	EE1 Power Electronics	a) Explain Ber Kho II - a

Time : 3 Hours

Maximum Marks : 80

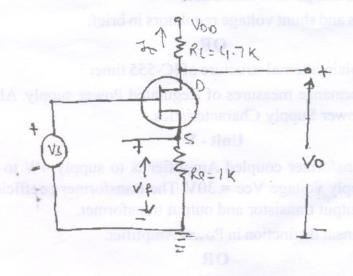
Min. Passing Marks : 24

Instructions to Candidates:

Attempt any **Five questions** selecting **one question** from **each unit**. All questions carry **equal marks**. (Schematic diagrams must be shown wherever necessary. Any data you feel missing may suitably be assumed and stated clearly.) Units of quantities used/ calculated must be stated clearly.

Unit - I

- a) Explain the effect of negative feedback on gain and frequency response of an complifier with the help of suitable diagram. (8)
 - b) Calculate D, $D_{\mu f}$, A_{vf} , R_{if} , R_{of} , $R_{o'f}$ for the circuit shown below (fig. 1) The FET parameters are μ =40 and rd = 40K. (8)



Or

- 1. a) An amplifier has a voltage gain of 200, before negative feed back is applied, when negative feedback with $\beta = 0.25$ is applied the nominal gain charges by two percent, find the small change in gain dAf/Af. (8)
 - b) State & explain stability criteria for feedback amplifier.

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(8)