3E1492

Roll No. : _____ Total Printed Pages : 4

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B. Tech. (Sem. III) (Main & Back) Examination, January - 2013
Electronics & Comm. (Common for Main & Back of 3EC2, 3AI2, 3EI2 & 3BM2)
3EC2 Electronics Devices & Circuits

Time : 3 Hours] [Total Marks : 80
[Min. Passing Marks : 24

Attempt any five questions, selecting one question from each unit. All questions carry equal marks. Schematic diagrams must be shown wherever necessary. Any data you feel missing suitably be assumed and stated clearly.
Units of quantities used/ calculated must be stated clearly.

Use of following supporting material is permitted during examination.
(Mentioned in form No. 205)

1. Nil 2. Nil

UNIT - I

1 (a) State the mass-action law as an equation and in words. Explain why a contact difference of potential must develop across an open circuited p-n junction. 12

(b) Explain the process of conductivity modulation. 4

OR

1 (a) What is diffusion ? Explain it with Einstein relation and derive continuity equation. 8

(b) Derive an expression for diffusion and drift currents. 8

UNIT - II

2 (a) A diode whose internal resistance is 35Ω is to supply power to a 1kΩ load from a 220V (rms) supply. Calculate :

(i) Peak load current

(ii) D.C. load current

(iii) A.C. load current

(iv) Diode voltage

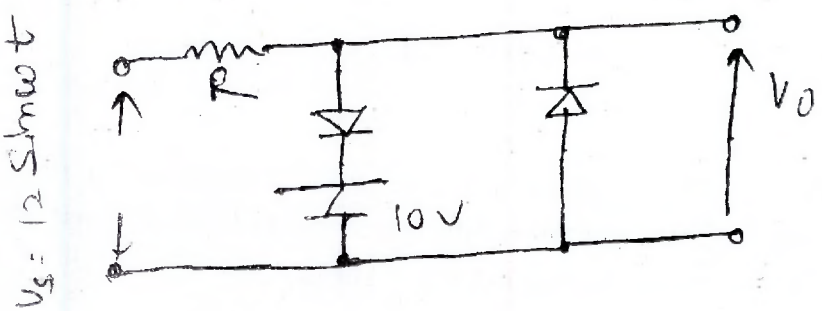
(v) Total input power to the circuit and

(vi) Percentage regulation from no load to given load. **10**

(b) Draw the characteristics of UJT and explain its working. **6**

OR

2 (a) Draw the output waveform for the circuit given.



6

(b) A full wave rectifier is to be designed to produce a peak output voltage 12 V and delivers a current of 120 mA to the load. It is required to restrict the ripple of not more than 5%. An Input line voltage is 120 V (rms), 60 Hz is available. **10**

UNIT - III

3 (a) Write the Ebers and Moll equations. Sketch the circuit model, which satisfies these equations. **10**

[Contd...

(b) Define stabilization techniques and compensation techniques. 6

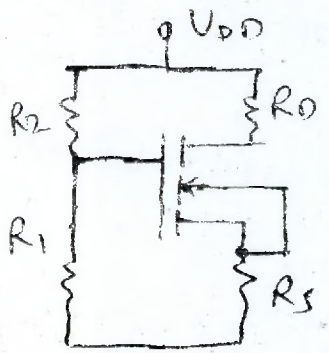
OR

3 (a) Discuss thermal runway and define thermal resistance. What is the condition for thermal stability ? - Explain. 8

(b) Explain base width modulation (the early effect) with the aid of plots of potential and minority concentration throughout the base region. 8

UNIT - IV

4 (a) The n-channel enhancement mode MOSFET of figure is characterized by $V_T = 4V$ and $I_{Don} = 10\,mA$. Assume negligible gate current, $R_1 = 50\,K\Omega$, $R_2 = 0.4M\Omega$, $R_s = 0$, $R_D = 2\,K\Omega$ and $V_{DD} = 15V$. Find (i) V_{GSQ} (ii) I_{OQ} and (iii) V_{DSQ}.



8

(b) Define the working of FET as voltage varible resistor. 8

OR

4 (a) Sketch the circuit of CS amplifier. Derive the expression for the voltage gain at low frequencies. What is the maximum value of A_v ? 10

(b) Draw the biasing circuit for a JFET or a depletion type MOSFET. Explain under what circumstances each of these two arrangements should be used. 6

UNIT - V

5 (a) Draw a Darlington emitter follower and explain, why the input impedance is higher than that of a single stage emitter follower. 10

(b) State Miller's theorem with the aid of a circuit diagram. Repeat for the dual of Miller's theorem. 6

OR

5 (a) Derive the expression for the CE short circuit current gain A_i as a function of frequency. 8

(b) Define f_β and f_T. What is the relationship between f_β and f_T ? 8
