4   (a)   What is races and what is process of race free assignment ?

8

(b)   Describe all type of Hazard in combination networks.

8

UNIT - V

5   Why we use FPGA kits and explain logic elements and programmability.

16

OR

5   Write short notes on : (any two)
(a)   SRAM
(b)   Flash Memory
(c)   Lookup Table Technology.

16