

5E3251-S

Roll No. : \_\_\_\_\_

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B. Tech. (Sem. V) (Main) Examination, December - 2011  
Computer Science  
5CS1 Computer Architecture (Common for Computer & IT)

Time : 3 Hours]

[Maximum Marks : 80  
[Min. Passing Marks : 24

**Instructions to Candidates :**

*Attempt any five questions selecting one question from each unit. All questions carry equal marks. Schematic diagrams must be shown wherever necessary. Any data you feel missing suitably be assumed and stated clearly. Units of quantities used/calculated must be stated clearly.*

Use of following supporting material is permitted during examination.  
(Mentioned in form No. 205)

1. \_\_\_\_\_ NIL \_\_\_\_\_

2. \_\_\_\_\_ NIL \_\_\_\_\_

**UNIT - I**

- 1 (a) Explain the following terms with reference to non Von-Neumann machines./
- (i) SISD
  - (ii) SIMD
  - (iii) MISD
  - (iv) MIMD

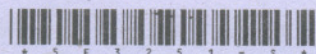
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- (b) A digital computer has a common bus system for 16 registers of 32 bits each. The bus is constructed with multiplexers.
- (i) How many selection input are there in each multiplexers ?
  - (ii) What size of multiplexer are needed ?
  - (iii) How many multiplexer are there in the bus ?

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OR

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