

OR

2. a) Discuss the various interrupts of 8085 μp . (8)
- b) Draw timing diagram for execution of the instruction 'STA 16 bit address'. Assume suitable data and show them in the diagram. (8)

Unit - III

3. a) Explain the formats of control words of PPI 8255 for
- i) I/O mode (8)
- ii) BSR mode (8)
- b) Interface 2 - chips of 4K byte RAM and 2 - chips of 2K byte ROM to μp 8085, by absolute decoding using 74LS138 decoder. Give the memory map starting at address 8000H for EPROM. (8)

OR

3. a) Draw the functional block diagram of 8254 IC and explain how this can be used to obtain a signal having frequency equal to 1/6 of I/P clock signal. (10)
- b) Illustrate the memory map of a chip with 512 - bytes of memory. (6)

Unit - IV

4. a) Write short note on Level converters -
MC 1488 and MC 1489 (8)
- b) Explain the block diagram of 8259 chip (8)

OR

4. a) Write short note on -
- i) RS 422n (8)
- ii) DMA controller (8)
- b) With the help of block diagram, explain the working of DMA controller. (8)