

**Time : 3 Hours** 

Maximum Marks : 80 Min. Passing Marks : 24

## Instructions to Candidates:

Attempt any five questions, selecting one question from each unit. All questions carry equal marks. (Schematic diagrams must be shown wherever necessary. Any data you feel missing suitably be assumed and stated clearly. Units of quantities used/calculated must be stated Clearly).

## Unit - I

| a) | Explain through block diagrams the similarity and differences in $\mu p$ ,<br>$\mu$ controller and $\mu$ computer devices. (6) |
|----|--|
| b) | Give the format of flag register in 8085 $\mu p$ . Explain each flag by giving suitable example. (5)                           |
| c) | Compare static RAM with dynamic RAM. (5)   |
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|    | the Freeking the Market dispersion of \$750 chin   |
| a) | Differentiate low - level and high - level languages. Also discuss their advantages and disadvantages. (8)                     |
| b) | Explain the basic operations of 8085 $\mu p$ . (8)   |
|    | Unit - II  |
| a) | If the clock frequency is 5MHz, how much time is required to execute an instruction of $18 \text{ T}$ - states (6)             |
|    | c)<br>a)<br>b)   |

b) Write a program to count continuously from FFH to OOH in a system with 0.5  $\mu$  sec clock period. Use register C to set up 1 ms delay b/w each count and display the numbers at one of the output ports. (10)

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